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TRANSMITTAL FORM (to be used for all correspondence after initial filing)			Application / Conf.	No.	10/039,204 / 2051	
			Filing Date		January 04, 2002 William C. Black	
			First Named Inven	tor		
			Examiner Name		Niketa I. Patel	
Mail Stop: AMENDMENT			Art Unit	•	2181	
Express Mail			Patent No.			
Receipt No. Total Number of Pages in This Submission			Attorney Docket N	umber	X-984 US	
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N THE UNITED STATES PATENT OFFICE

Applicants: William C. Black et al.

Assignee: Xilinx, Inc.

Title: Distributed Buffering System having Programmable Interconnecting

Logic and Applications Thereof

Serial No.: 10/039,204 File Date: 01/04/2002

Examiner: Niketa I. Patel Art Unit: 2181

Docket No.: X-984 US Conf. No.: 2051

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SECOND RESPONSE TO FIRST RESTRICTION REQUIREMENT

Dear Madam:

This Response and Election is filed herewith in response to the Office Communication dated September 20, 2006, having a reply period set to expire on October 20, 2006.

It was stated in the previous Office Action that the application contains the following eight separate patentably distinct species:

Species I: Figure. 4

Species II: Figure. 5

Species III: Figure. 6

Species IV: Figure. 7

Species V: Figure. 8

Species VI: Figure. 9

Species VII: Figure. 10

Species VIII: Figure. 11